



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: 1765

Examiner: K. Chen

In re Application of:

SHINTANI et al.

Application No. 09/934,453

Filed: August 22, 2001

For: METHOD OF FABRICATING A

SEMICONDUCTOR DEVICE INCLUDING TIME-SELECTIVE

**ETCHING PROCESS** 

## SUPPLEMENTAL AMENDMENT

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In supplementation of the Amendment filed January 23, 2004, please amend the claims as follows.